

FIG. 1

Patent 6,666,666

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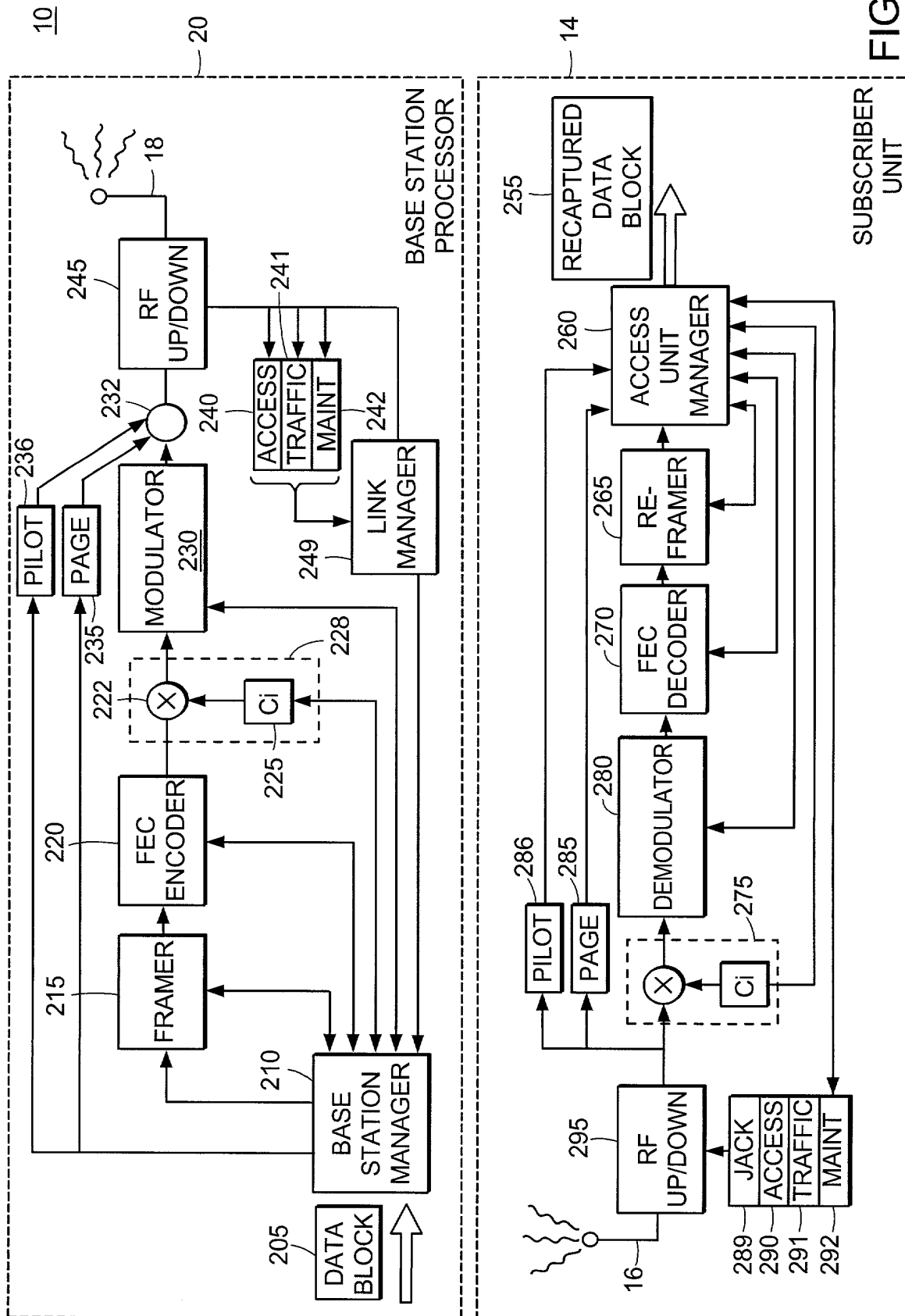


FIG. 2



FIG. 3

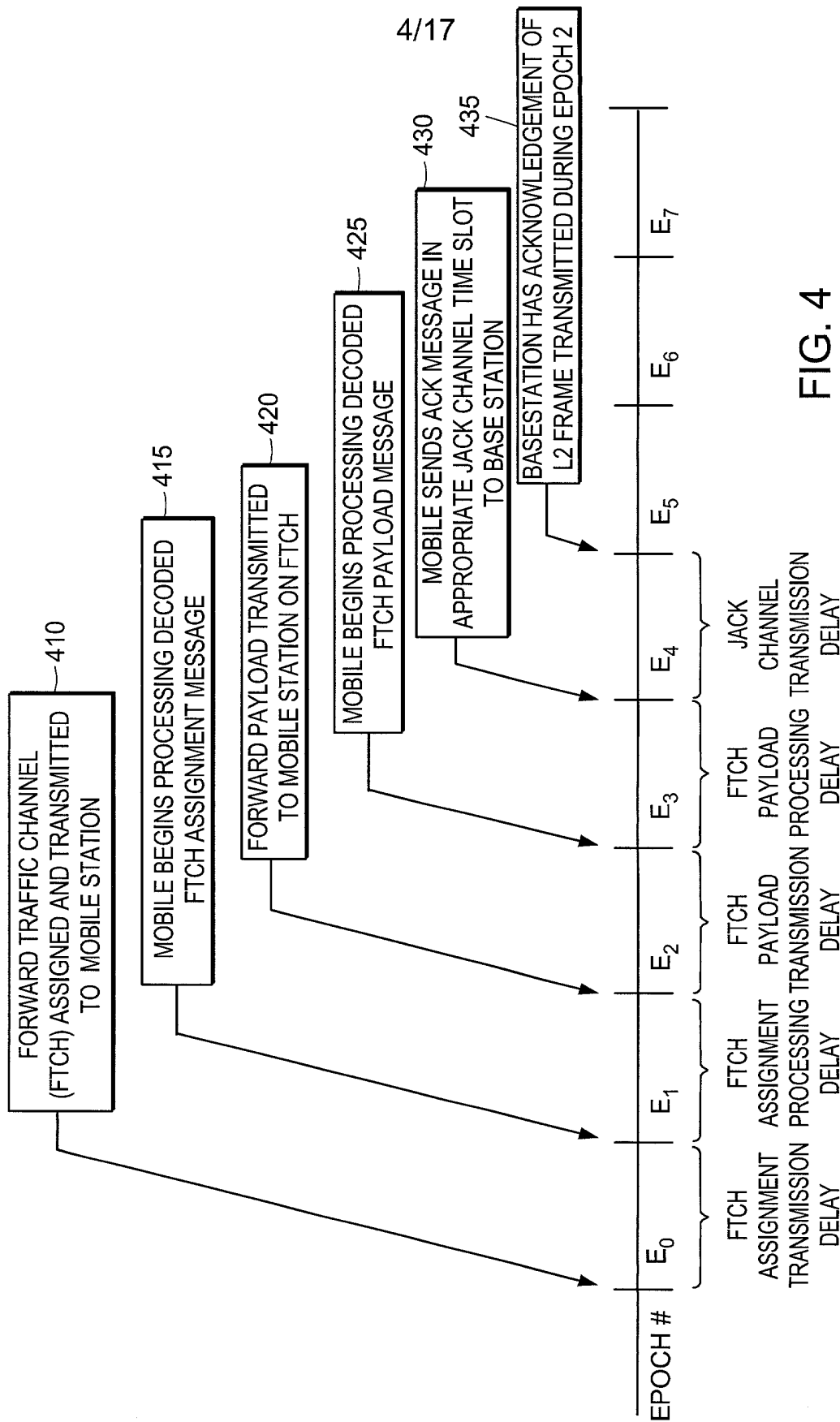


FIG. 4

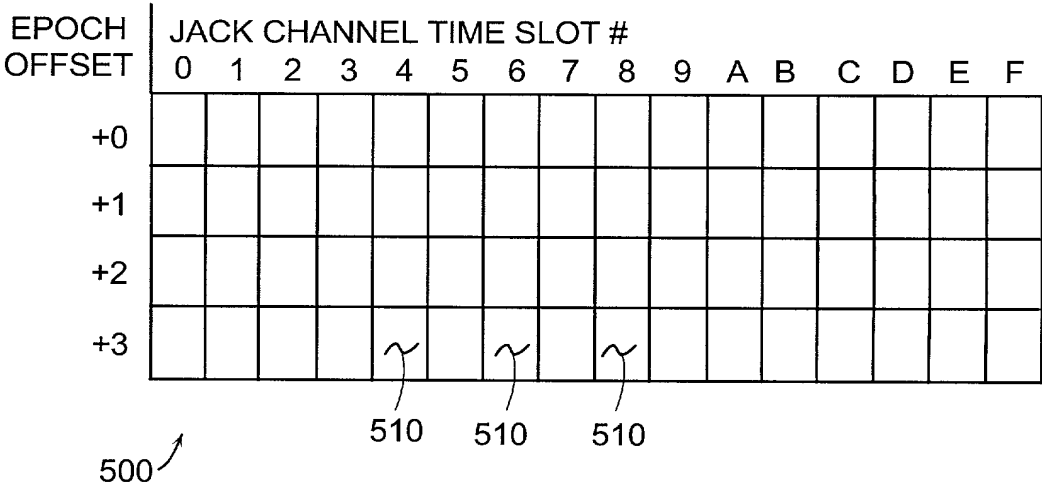


FIG. 5

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LAYER 4 STREAM ID	32-BIT SOURCE ADDRESS	32-BIT DESTINATION ADDRESS	16-BIT SOURCE PORT	16-BIT DESTINATION PORT
0001	~	~	~	~
0002	~	~	~	~
0003	~	~	~	~
0004	~	~	~	~

FIG. 6

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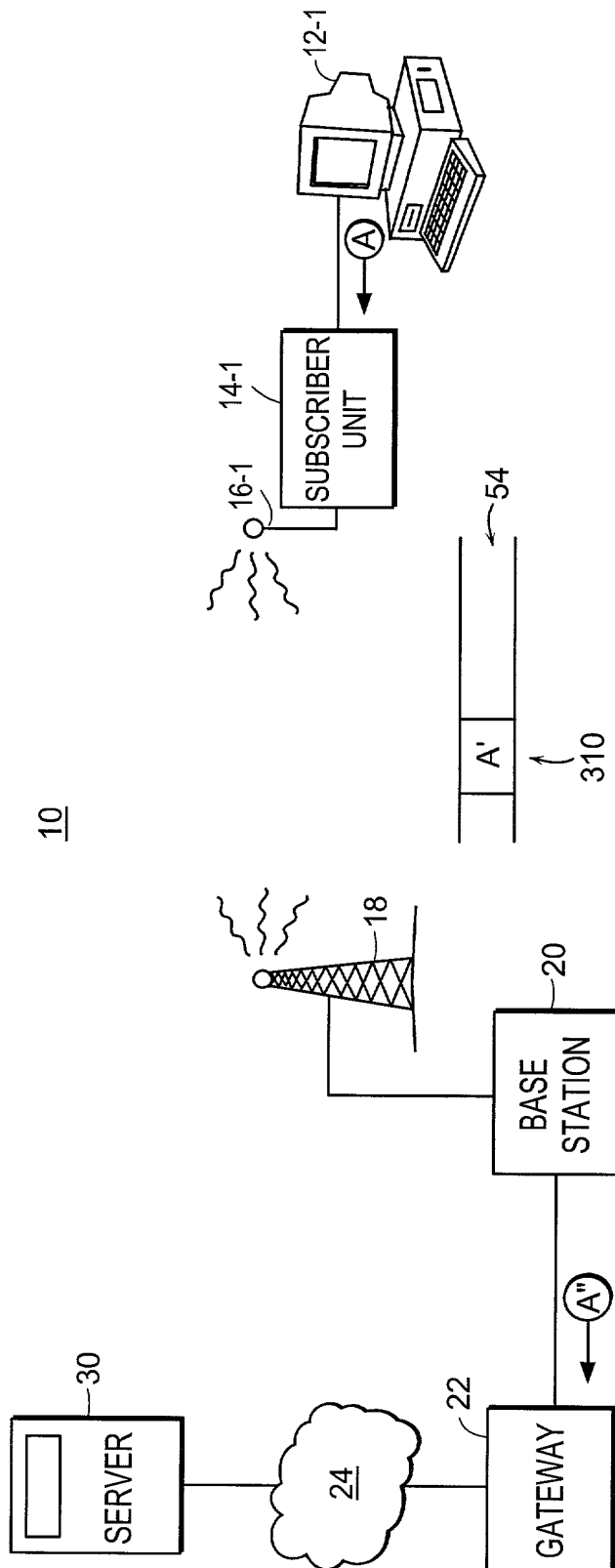


FIG. 7

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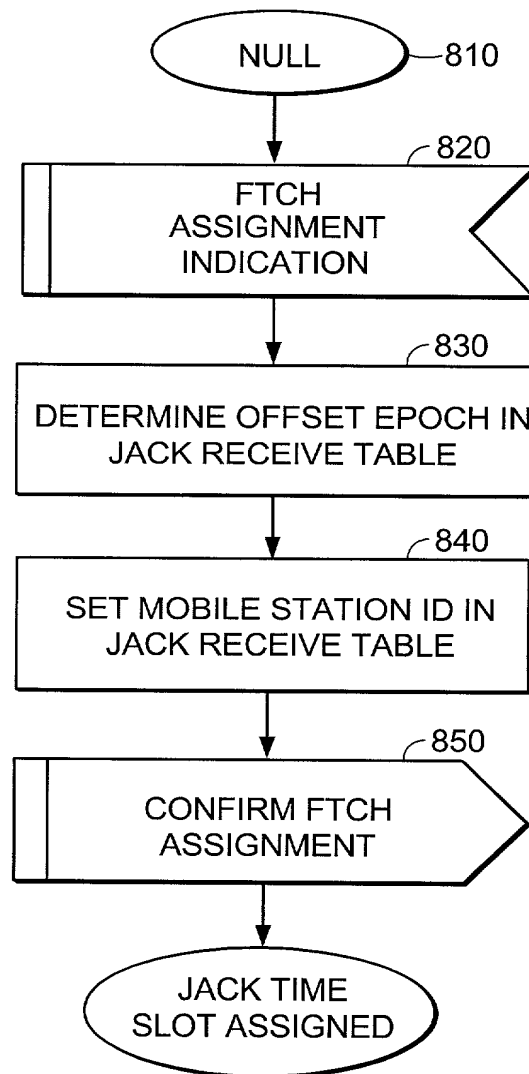


FIG. 8

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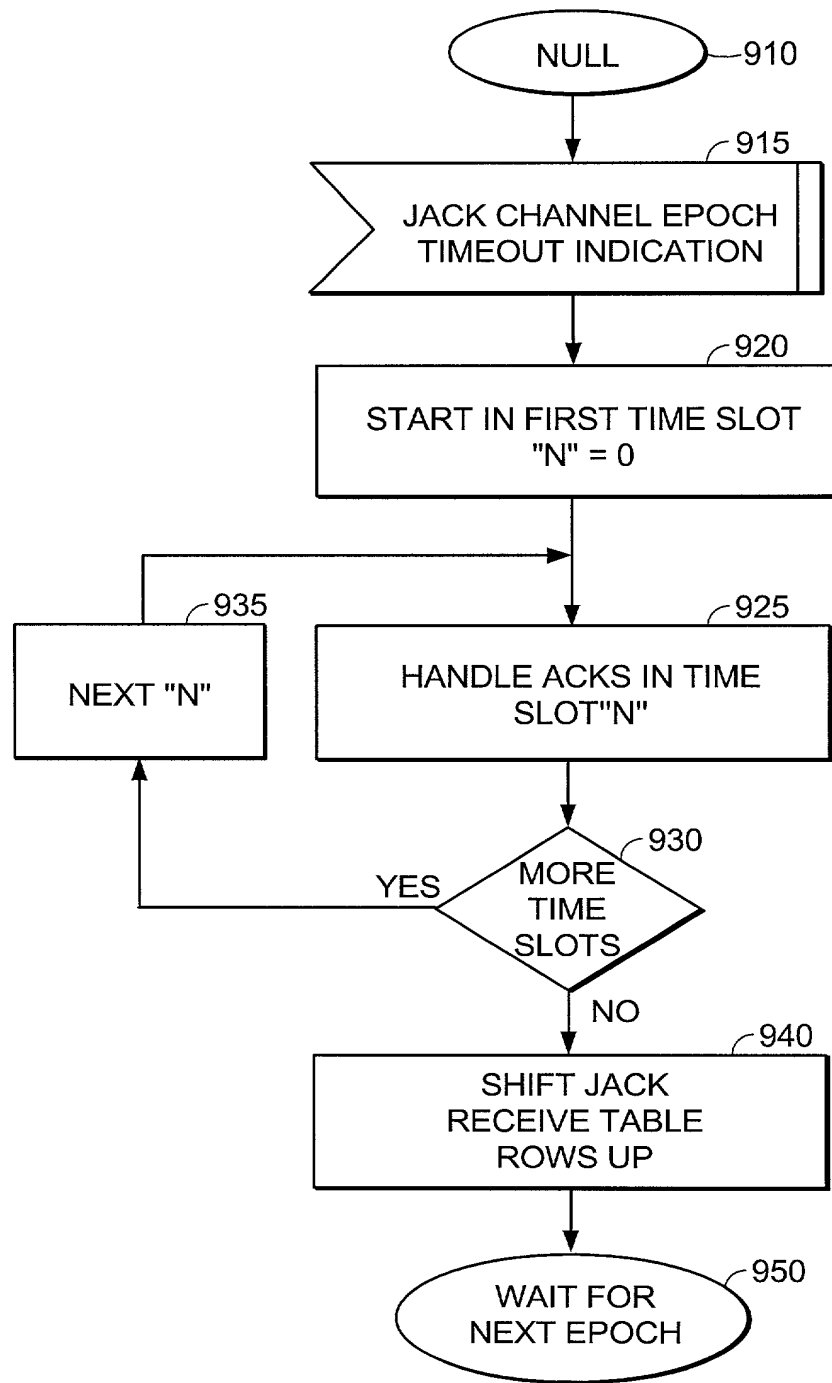


FIG. 9

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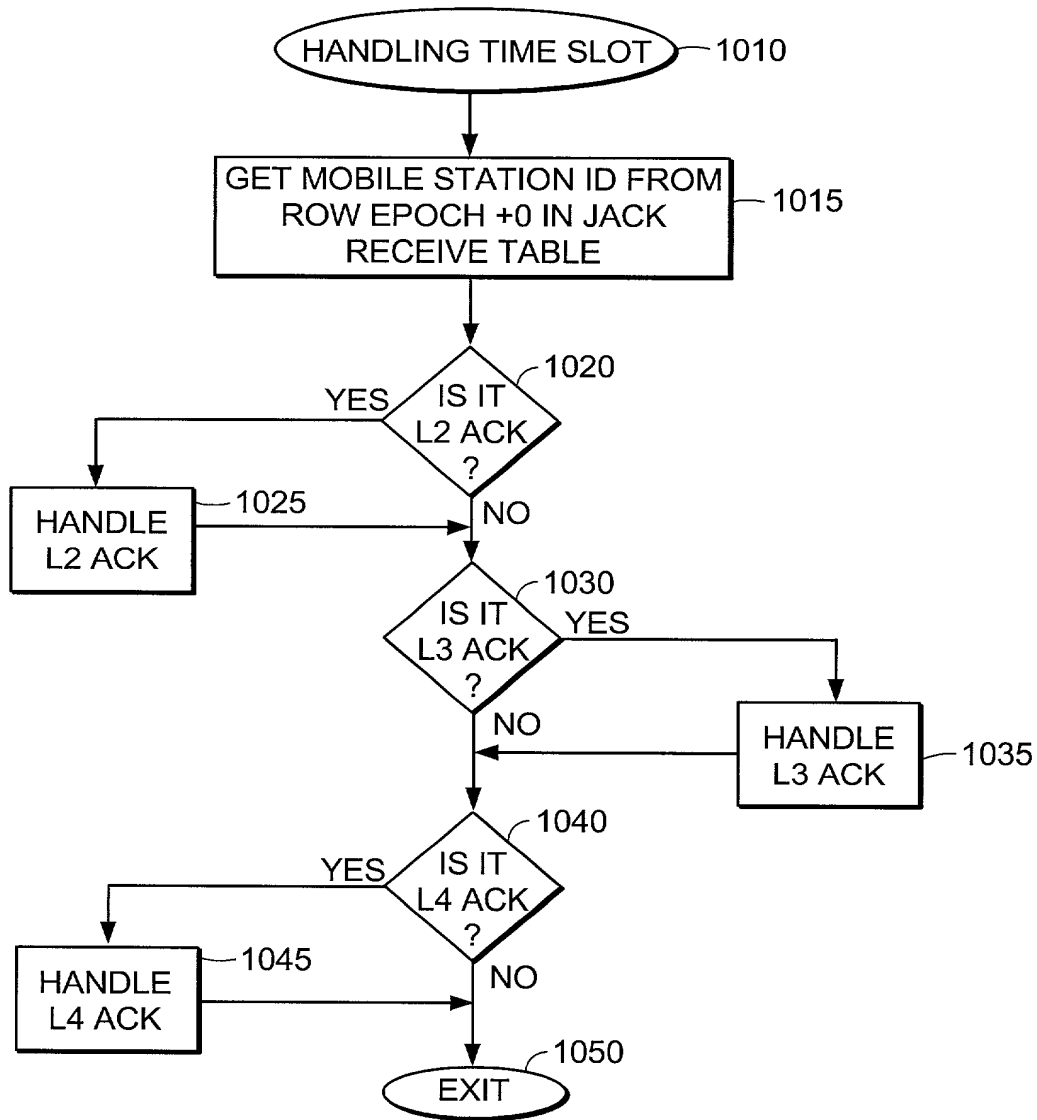


FIG. 10

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FIG. 11

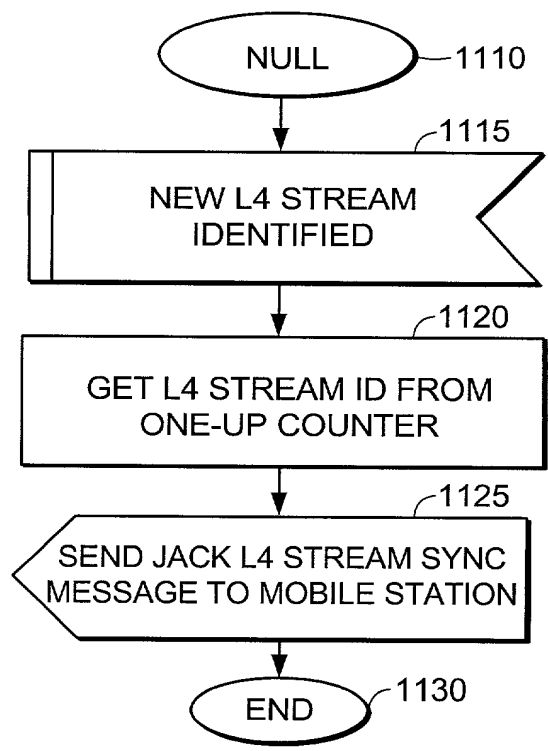
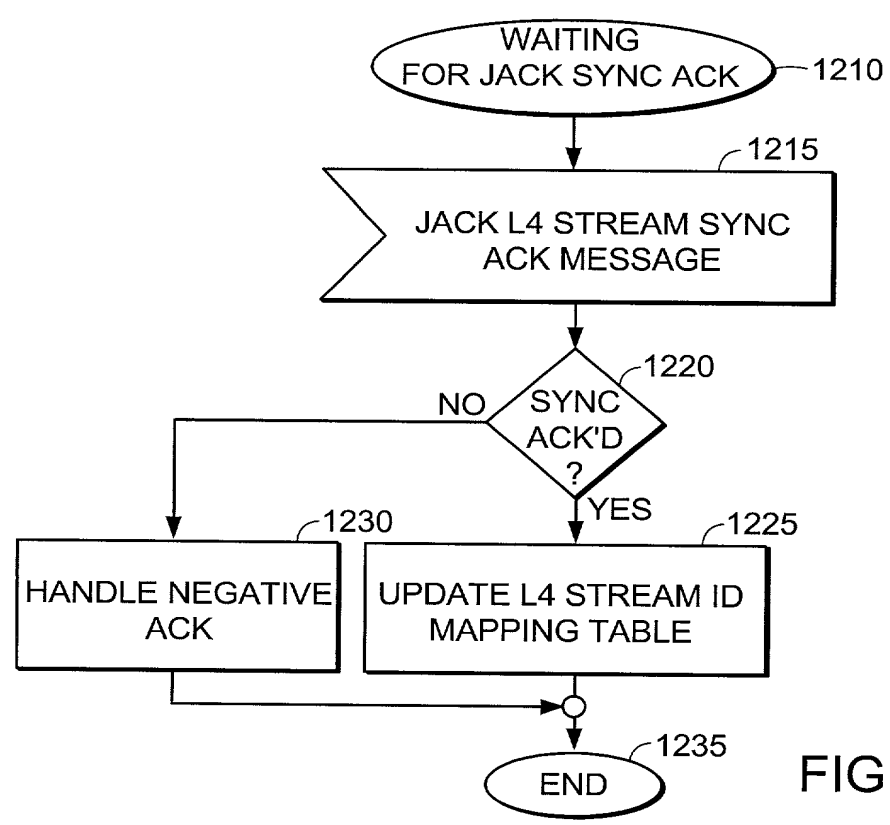


FIG. 12



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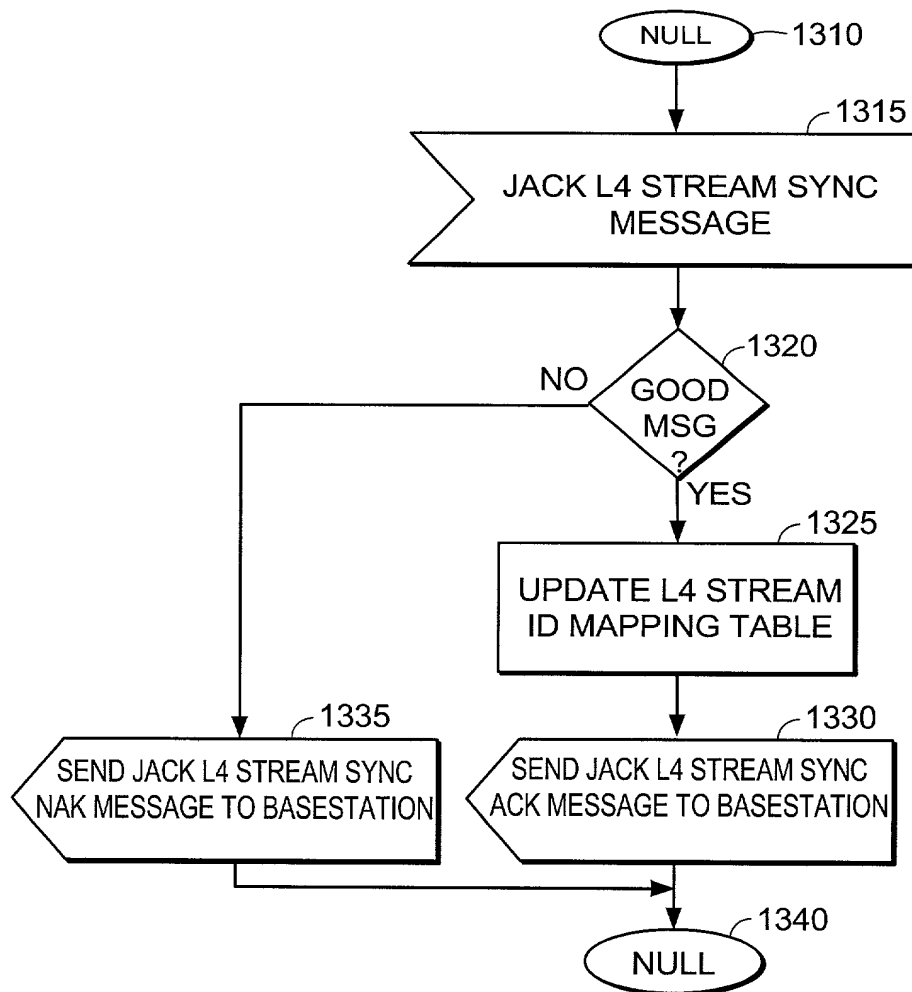


FIG. 13

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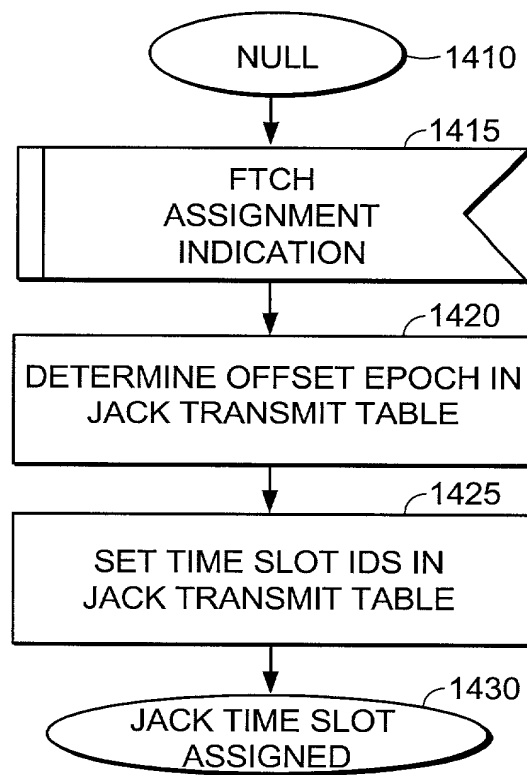


FIG. 14

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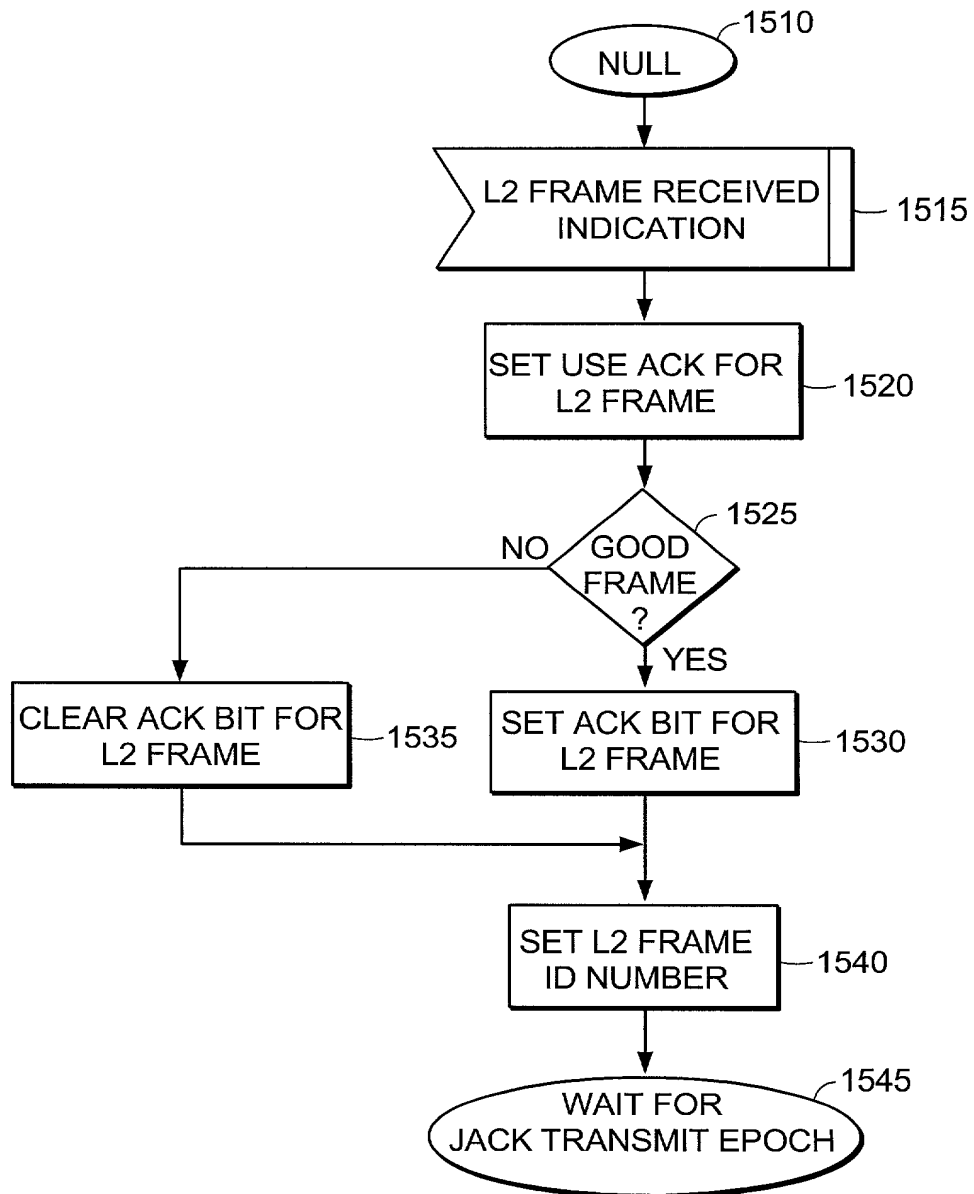


FIG. 15

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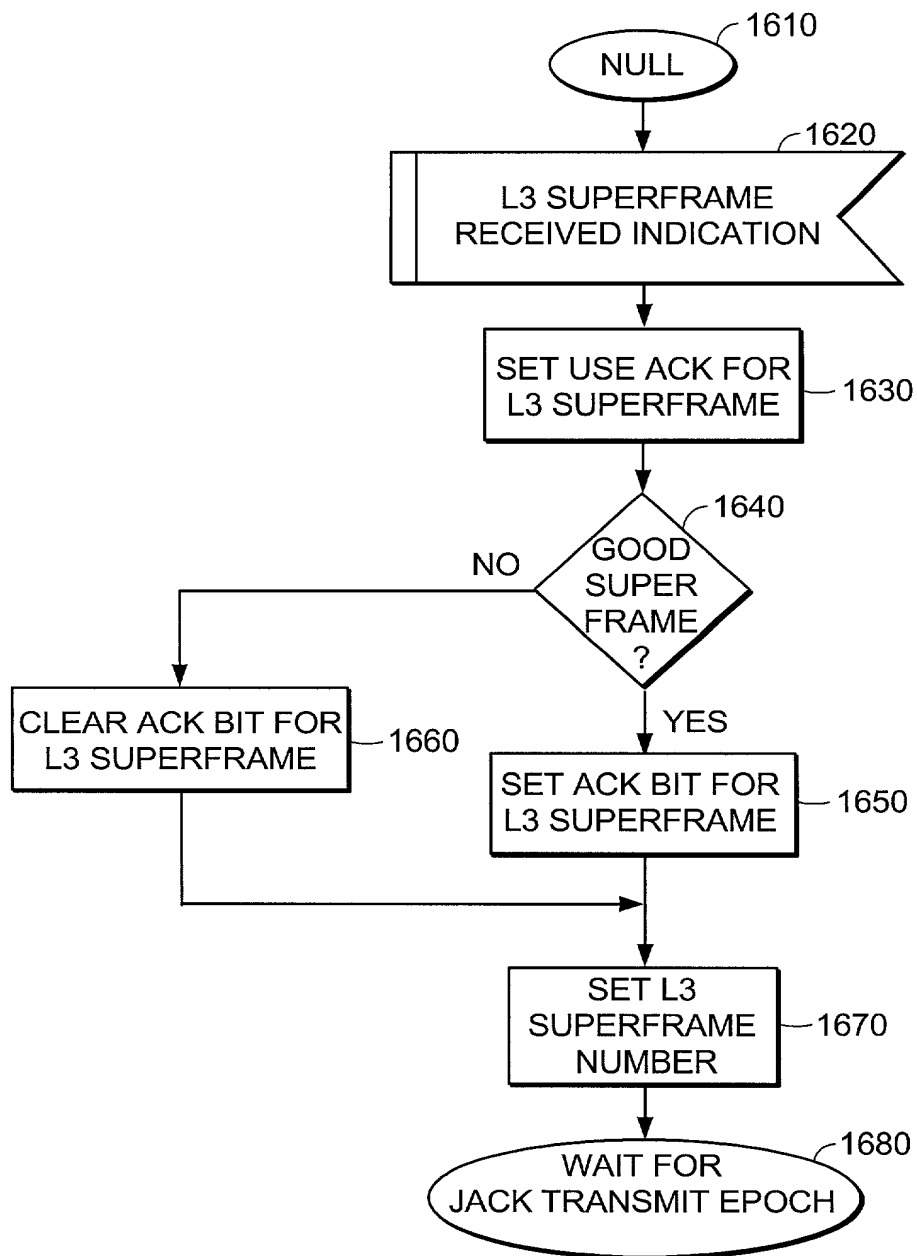


FIG. 16

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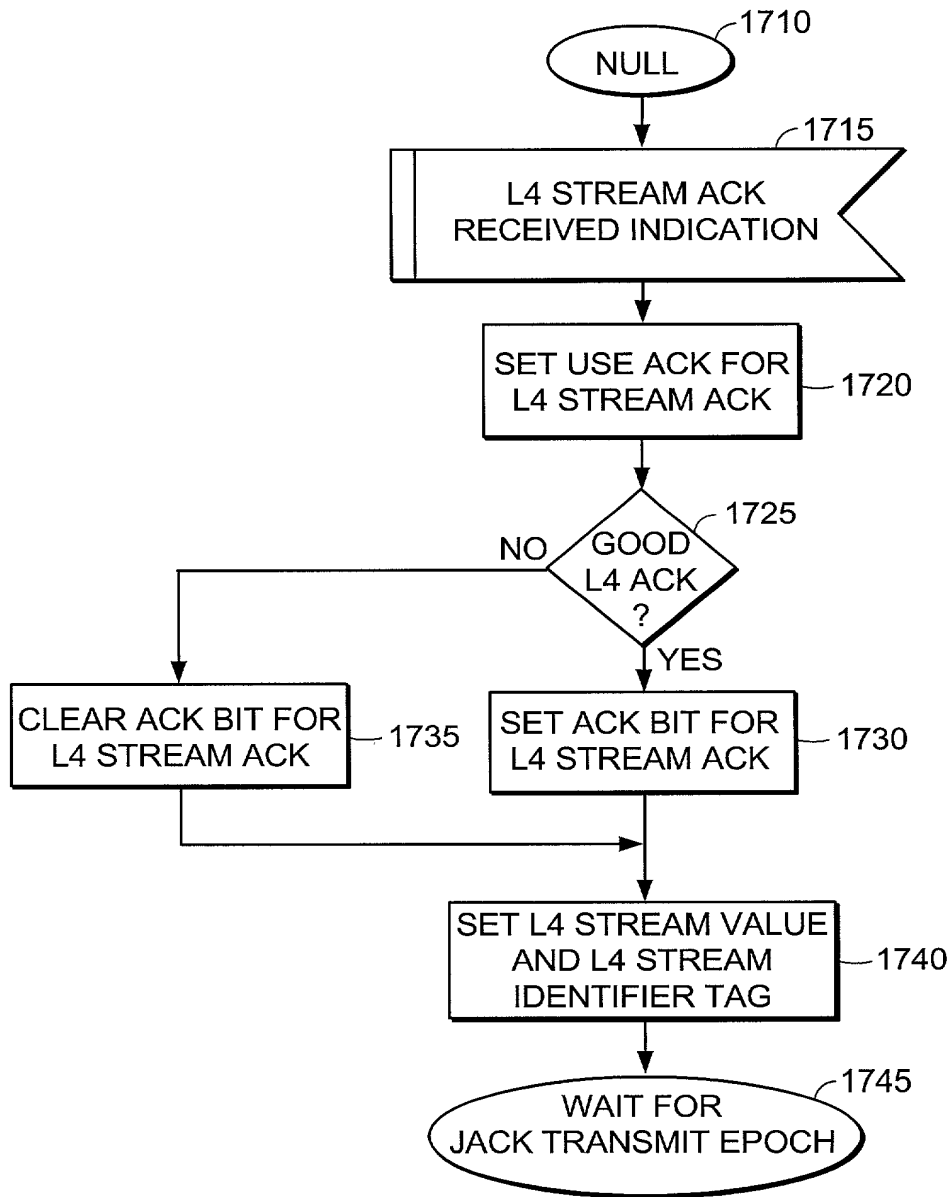


FIG. 17

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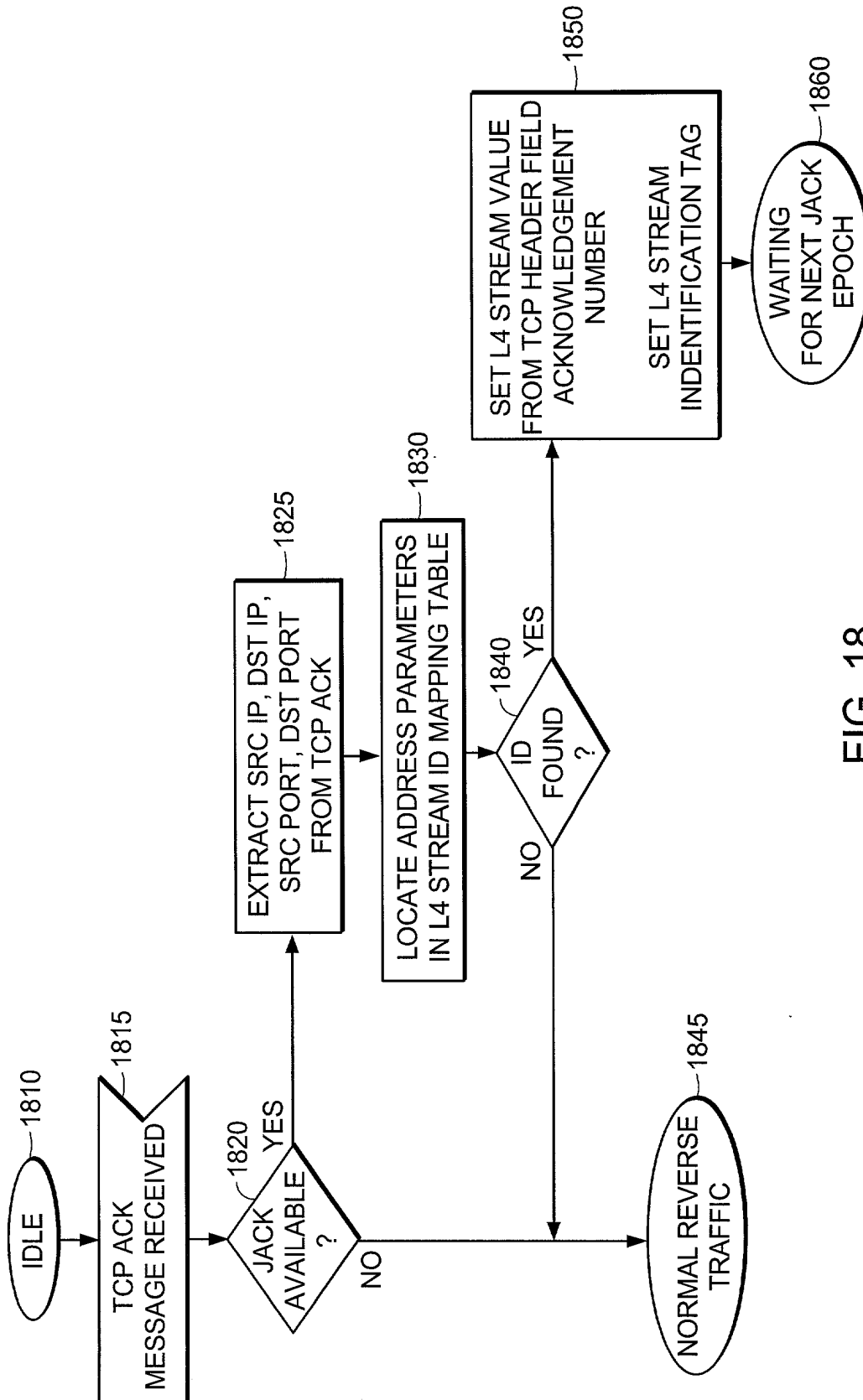


FIG. 18

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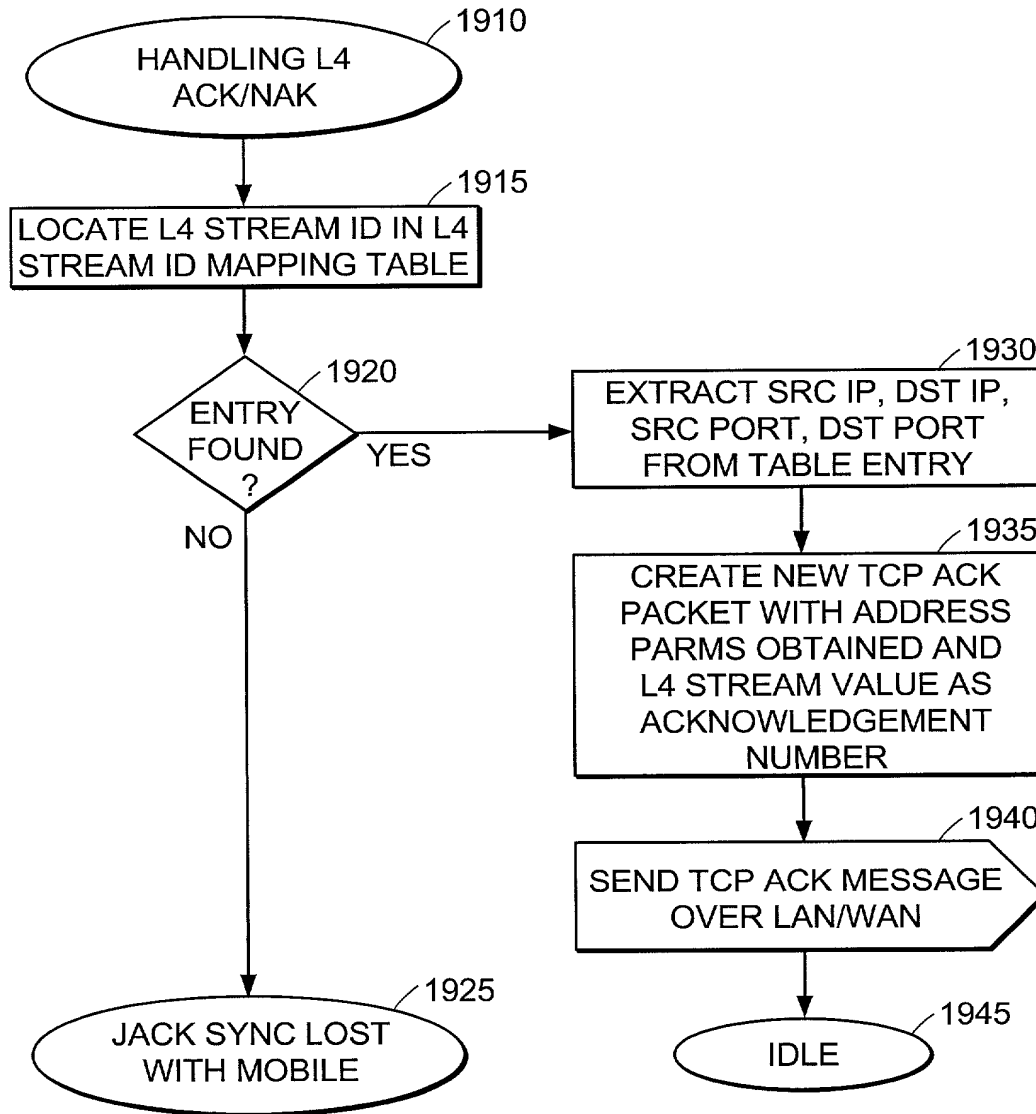


FIG. 19